

KENNETH E. JAMES

Home: (408) 225-4341
7032 Via Serena, San Jose, CA 95139

Work: (408) 447-3543
kemejames@sbcglobal.net

OBJECTIVE Seeking Opportunities as a Software/Hardware Design Engineer

EXPERIENCE **DESIGN ENGINEER** September 1988 – Present

March 2001 – present

*Hewlett Packard Company, NonStop Enterprise Division & BCS Vertical Growth Lab,
Cupertino, California*

- Board-level design, bring-up, and debugging of high-end fault-tolerant computer servers
- Analog, power, and digital designs; Verilog designs of CPLD logic

August 1997 – February 2001

Evans & Sutherland Computer Corp. Salt Lake City, Utah

- Board-level design of high-speed digital and analog video processing circuitry, including frequency synthesizer design; video microcode development
- System-level integration and design of high-speed cables

October 1996 - August 1997

MagnaTronics (self-employed) Magna, Utah

- Contract work with micromachining company to develop micro-EDM (electrical discharge machining) pulse generator

September 1988 - October 1996

AccuData, Inc. (defunct) Salt Lake City, Utah

- Board-level design of data entry terminals and data collection computers
- Microprocessor and microcontroller circuit design
- Battery-powered and off-line power supply design
- Lightning-protected serial communications
- Embedded software design
- Relational database design
- Host PC application software design (interface to data collection system)

ELECTRONICS TECHNICIAN 1974 - 1988

1983 – 1987 Self-employed, Provo, Utah

1984 – 1986, Getty Oil Company (now Texaco), Bakersfield, California

1982 – 1986 Airport TV, Bakersfield, California

1974 – 1983 Self-employed, Bakersfield, California

1979 – 1979 AudioTronics (defunct), Bakersfield, California

- Repaired audio, video, telecommunications, and power supply equipment part-time while attending school and full-time during school breaks
- Performed electronic and mechanical troubleshooting, repairs, and installations

SKILLS

- High-speed digital/analog board design
- Embedded software design (6809, 68HC11, 8051 assembler)
- Object-oriented software design (Visual Basic, Clipper, some C/C++)
- Use of schematic capture and printed circuit board layout software (ViewDraw, Allegro)
- Analog circuit simulation with HSPICE and AvanWAVES
- Verilog and ABEL design of FPGAs and CPLDs (Xilinx, Altera, Synplify)

EDUCATION **BRIGHAM YOUNG UNIVERSITY** Provo, Utah, BSEE, 1983 - 1988
GPA 3.27/4.0 Major GPA 3.41/4.0 Emphasis on electronics

REFERENCES Available upon request